

LECTURE 3

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4. Frequency Response of Electronic Circuits

4.1 Frequency Response of Linear Systems

4.2 Frequency Response of Elementary Transistor Circuits

4.3 Cascode Gain Stage

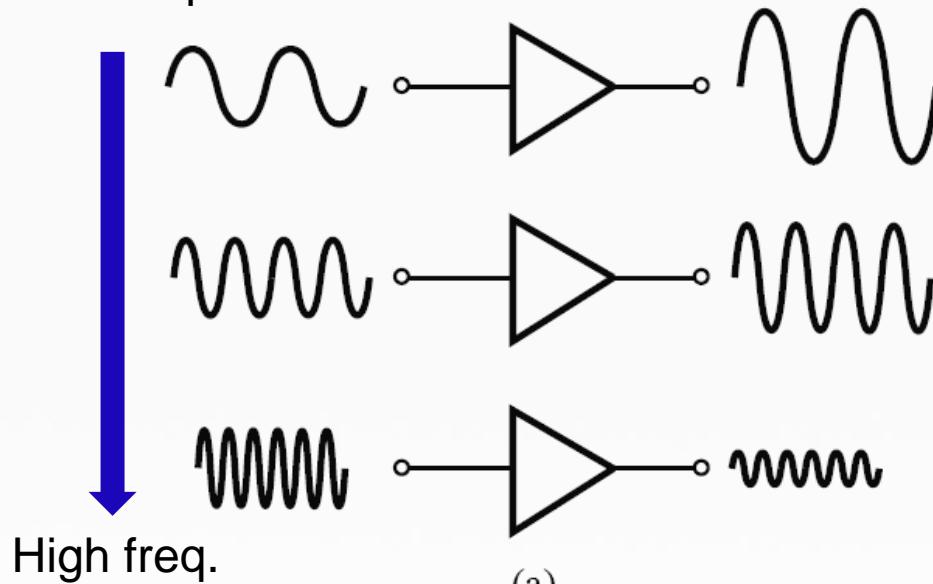
4.4 Source-Follower Amplifier

4.5 Differential Pair



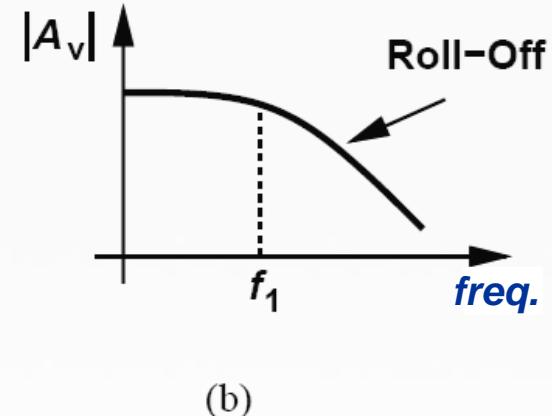
Gain Roll-off of Amplifier

Low freq.



(a)

$|A_v|$
Large
↓
Small.

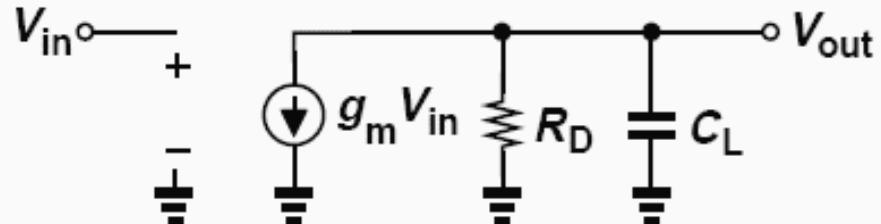
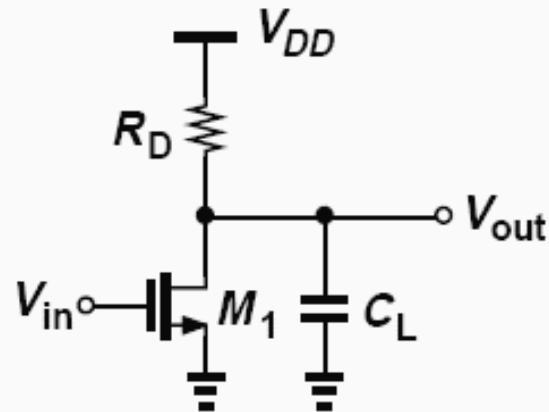


(b)

$$\text{freq.} \uparrow \Rightarrow |A_v| \rightarrow 0$$

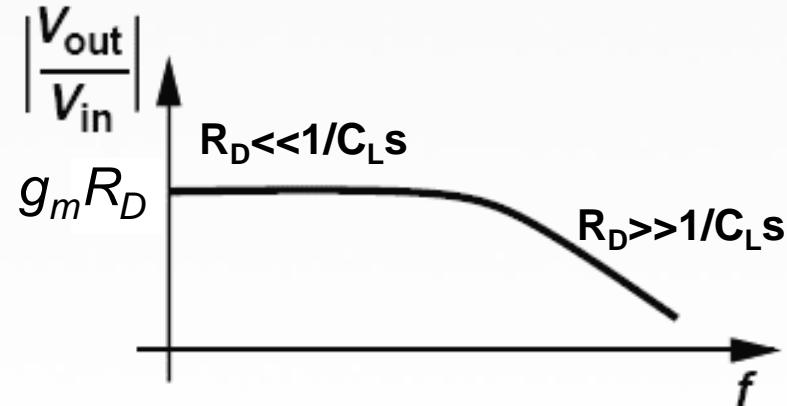


Gain Roll-off: Common Source



$$V_{out} = -g_m V_{in} \left(R_D \parallel \frac{1}{C_L s} \right)$$

$f \uparrow \Rightarrow \frac{1}{C_L s} = 0 \Rightarrow A_v \rightarrow 0$



Bode Plot

Recall the electrical circuit theorem

$$H(s) = A_0 \frac{\left(1 + \frac{s}{\omega_{z1}}\right)\left(1 + \frac{s}{\omega_{z2}}\right)\dots}{\left(1 + \frac{s}{\omega_{p1}}\right)\left(1 + \frac{s}{\omega_{p2}}\right)\dots}$$

ω_z : zero frequency

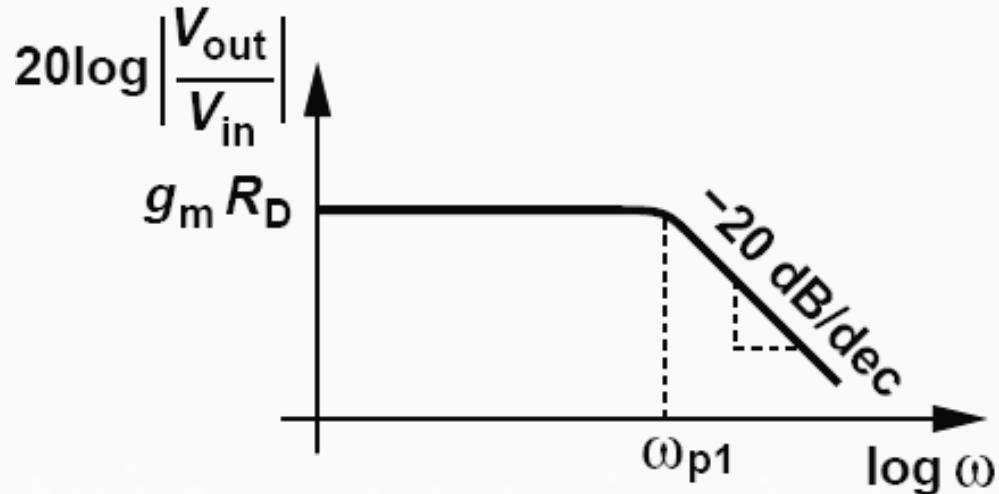
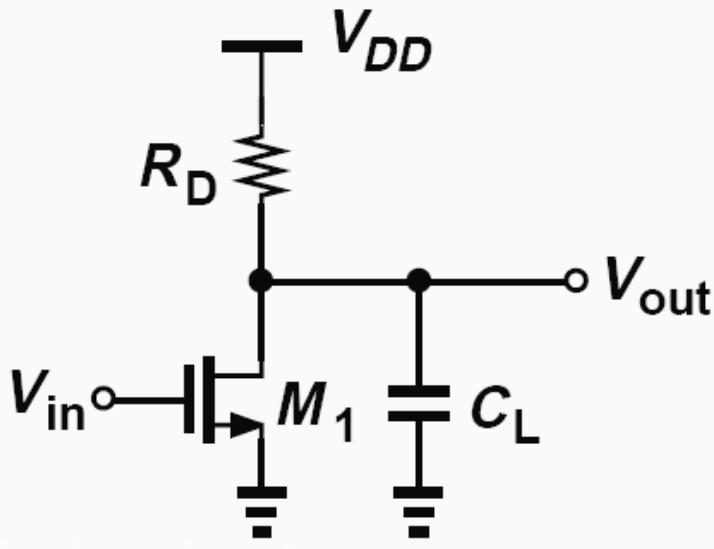
➤ Increase by **20dB/decade**

ω_p : pole frequency

➤ Decrease by **20dB/decade**



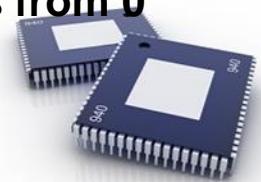
Example: Bode Plot



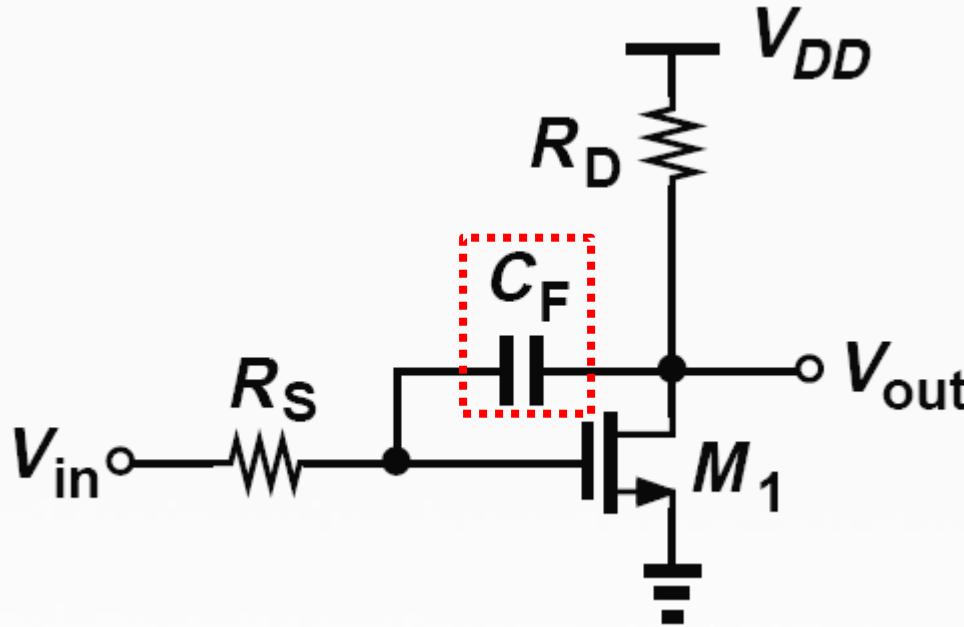
$$\frac{V_{out}}{V_{in}} = \frac{-g_m R_D}{1 + \frac{s}{1/R_D C_L}}$$

$$|\omega_{p1}| = \frac{1}{R_D C_L}$$

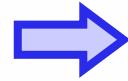
The circuit only has one pole (no zero) at $1/(R_D C_L)$, so the slope drops from 0 to -20 dB/dec as ω pass ω_{p1} .



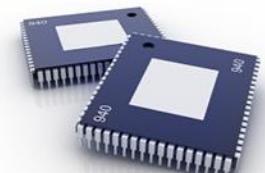
Circuit with Floating Capacitor



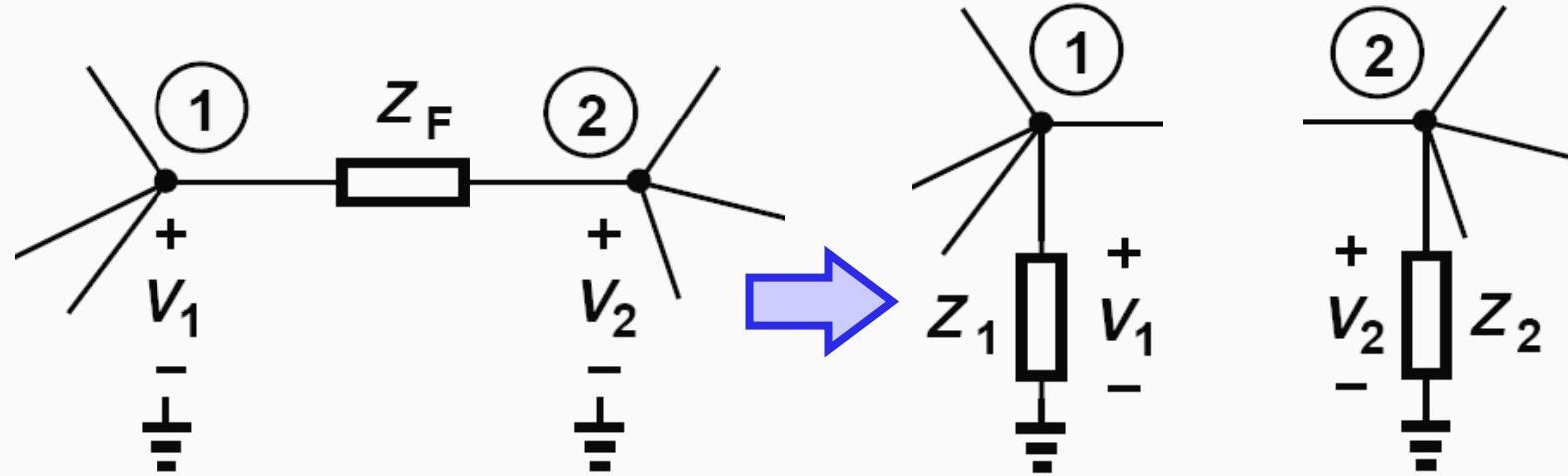
The **pole** of a circuit is computed by finding the **effective resistance** and **capacitance** from a node to GROUND.



Miller's theorem



Miller's Theorem



$$\frac{V_1 - V_2}{Z_F} = \frac{V_1}{Z_1}$$

$$\frac{V_2 - V_1}{Z_F} = \frac{V_2}{Z_2}$$

$$A_v = \frac{V_2}{V_1}$$

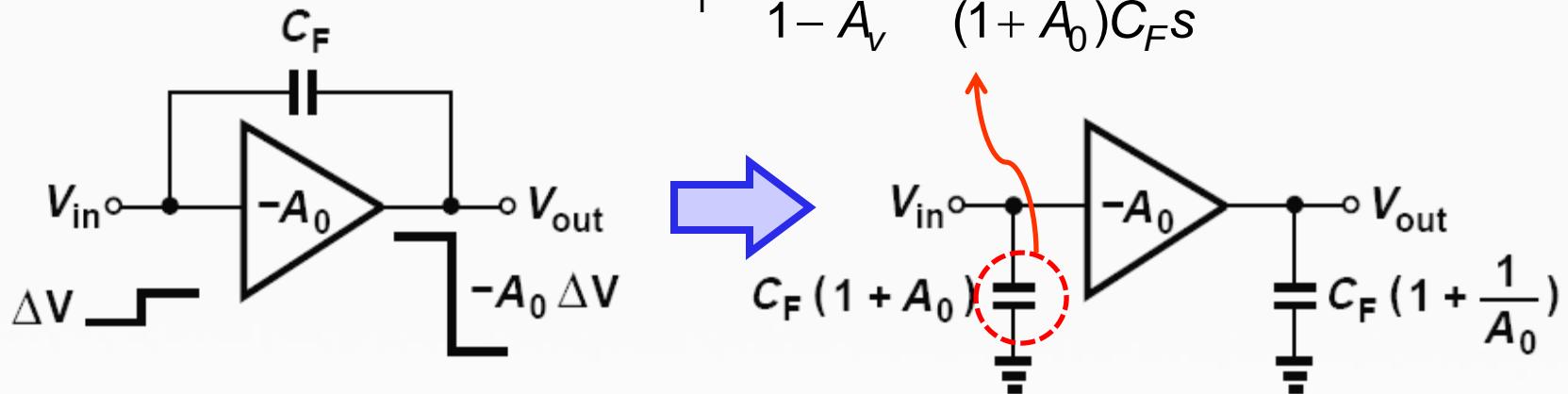
$$Z_1 = \frac{Z_F}{1 - A_v}$$

$$Z_2 = \frac{Z_F}{1 - 1/A_v}$$



Miller's Multiplication

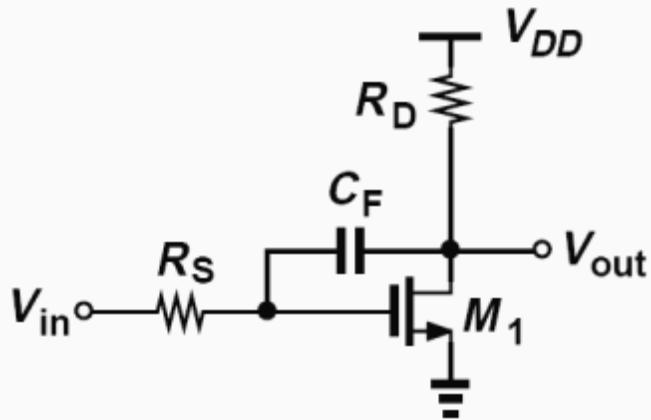
$$Z_1 = \frac{Z_F}{1 - A_v} = \frac{1}{(1 + A_0)C_F s}$$



The input capacitor is larger than the original floating capacitor
(Miller multiplication)



Example: Miller Theorem



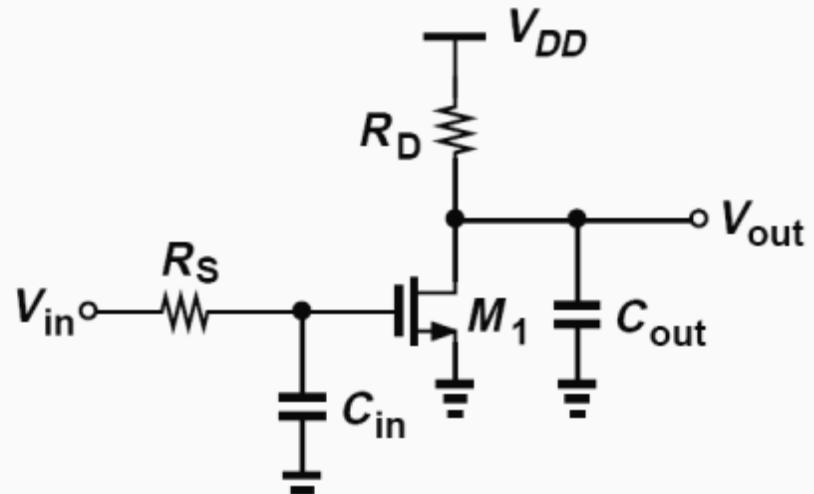
$$A_0 = -g_m R_D$$

$$C_{in} = (1 - A_0)C_F = (1 + g_m R_D)C_F$$

$$C_{out} = (1 - 1/A_0)C_F = \left(1 + \frac{1}{g_m R_D}\right)C_F$$

$$\omega_{p,in} = \frac{1}{R_S \cdot C_{in}} = \frac{1}{R_S (1 + g_m R_D) C_F}$$

$$\omega_{p,out} = \frac{1}{R_D \cdot C_{out}} = \frac{1}{R_D \left(1 + \frac{1}{g_m R_D}\right) C_F}$$



$$\frac{V_{out}(s)}{V_{in}} = \frac{-g_m R_D}{\left(1 + \frac{s}{\omega_{p,in}}\right)\left(1 + \frac{s}{\omega_{p,out}}\right)}$$

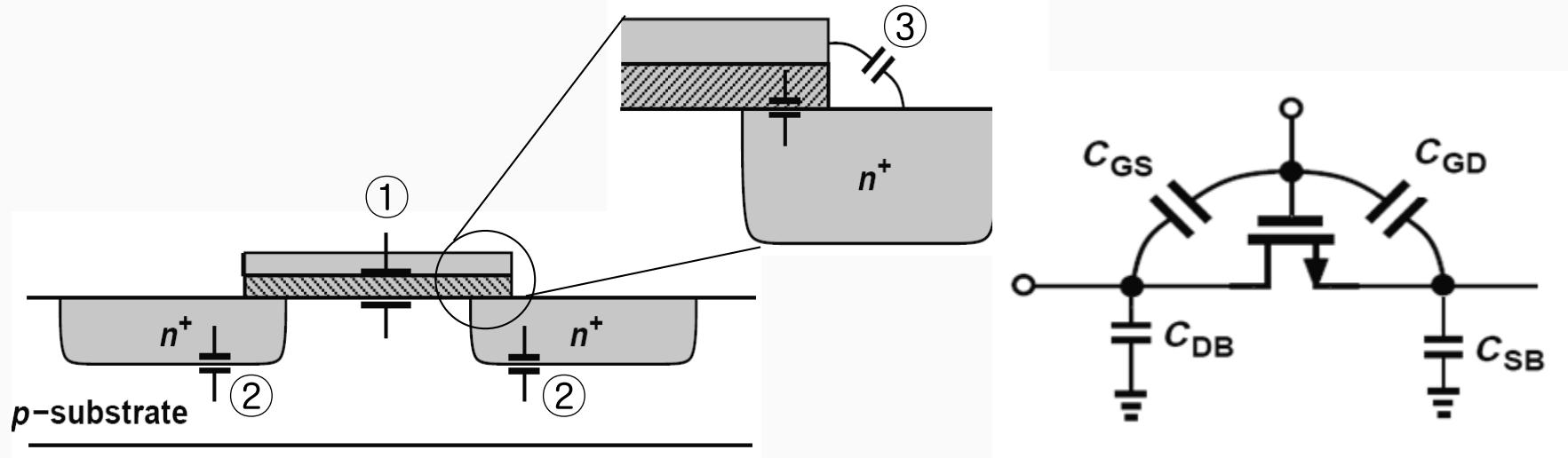


Demerits

1. Discard zeros
2. Approximating gain

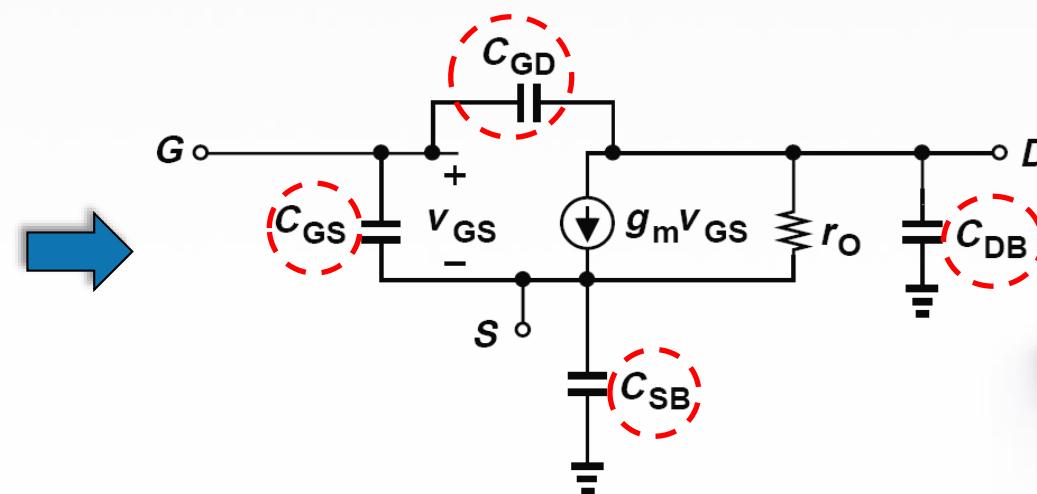


MOS Intrinsic Capacitances ?



①Oxide capacitance ② Junction capacitances ③ Overlap capacitance

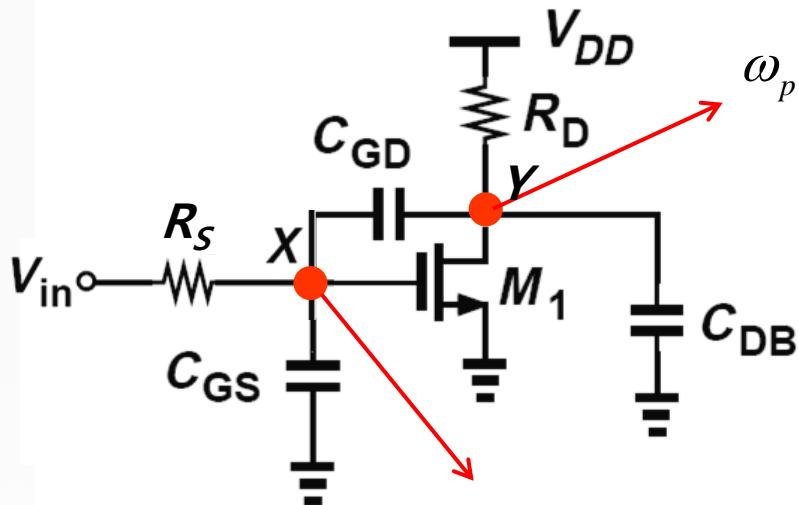
Small-signal
equivalent circuit



Frequency Response of CS Stage

Use the Miller theorem

$$r_o = \infty$$



$$\omega_{p,Y} = \frac{1}{R_D \left[C_{DB} + \left(1 + \frac{1}{g_m R_D} \right) C_{GD} \right] s}$$

Miller theorem

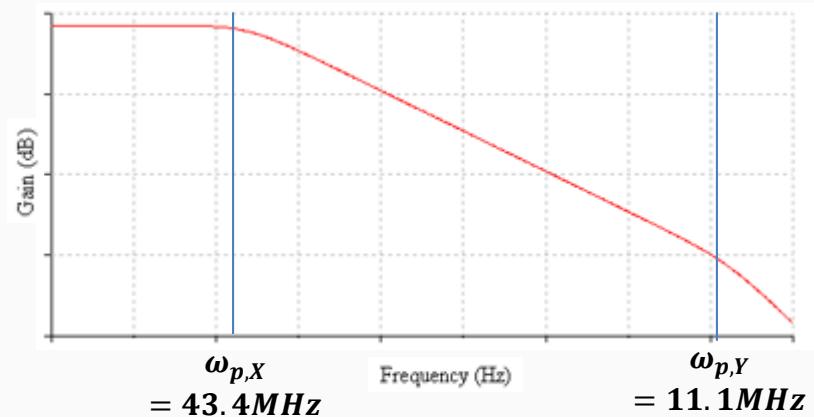
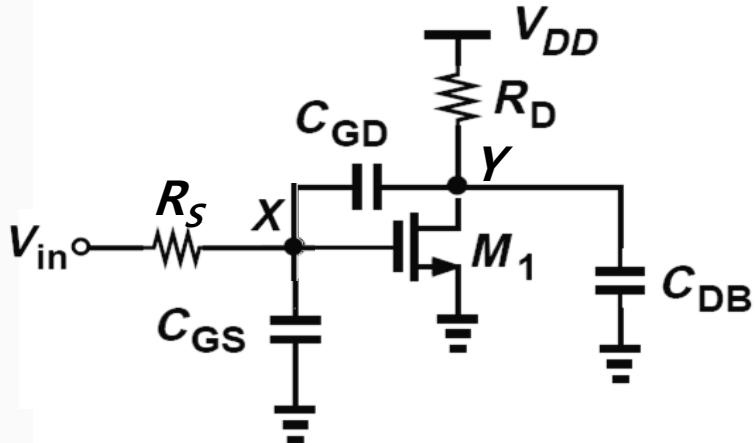
$$\omega_{p,X} = \frac{1}{R_s \left[C_{GS} + (1 + g_m R_D) C_{GD} \right] s}$$

Miller theorem



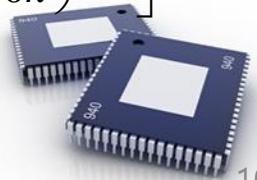
Example 1

Calculate the $\omega_{p,X}$, $\omega_{p,Y}$ (if, $g_m = 150\mu A/V$, $R_s = 100k\Omega$, $R_D = 40k\Omega$, $C_{GS} = 23fF$, $C_{GD} = 2fF$, $C_{DB} = 35fF$)



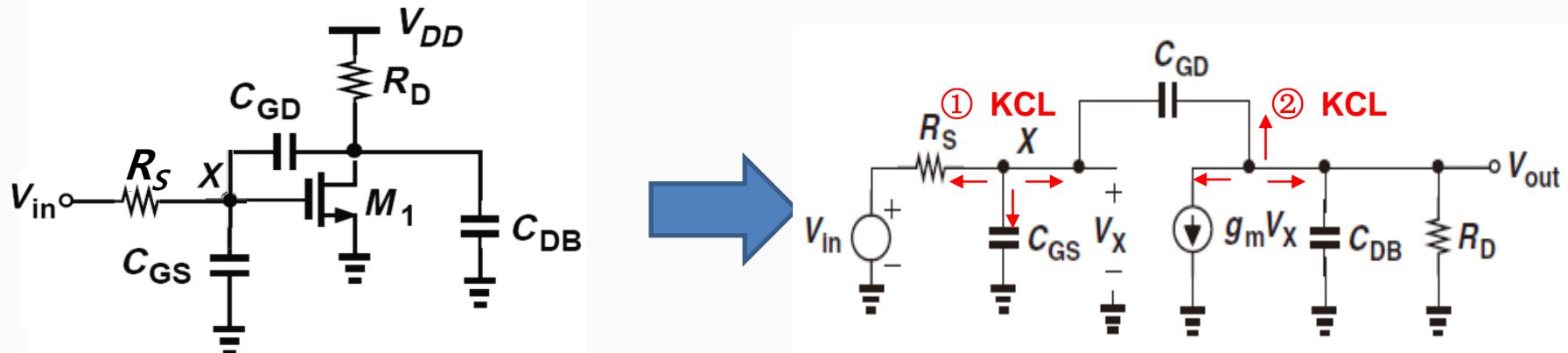
$$\begin{aligned}\omega_{p,X} &= \frac{1}{R_s [C_{GS} + (1 + g_m R_D) C_{GD}] s} \\ &= \frac{1}{100k \cdot [23f + (1 + 150\mu \cdot 40k) \cdot 2f] \cdot 2\pi} \\ &= \frac{1}{100k \cdot [23f + (1 + 150\mu \cdot 40k) \cdot 2f] \cdot 2\pi} \\ &= 43.4 \text{ MHz}\end{aligned}$$

$$\begin{aligned}\omega_{p,Y} &= \frac{1}{R_D [C_{DB} + \left(1 + \frac{1}{g_m R_D}\right) C_{GD}] s} \\ &= \frac{1}{40k \cdot \left[35f + \left(1 + \frac{1}{150\mu \cdot 40k}\right) 2f\right] \cdot 2\pi} \\ &= 111.1 \text{ MHz}\end{aligned}$$



Frequency Response of CS Stage

Use the equivalent circuit



$$\textcircled{1} \quad \frac{V_x - V_{in}}{R_s} + V_x C_{GS}s + (V_x - V_{out}) C_{GD}s = 0$$

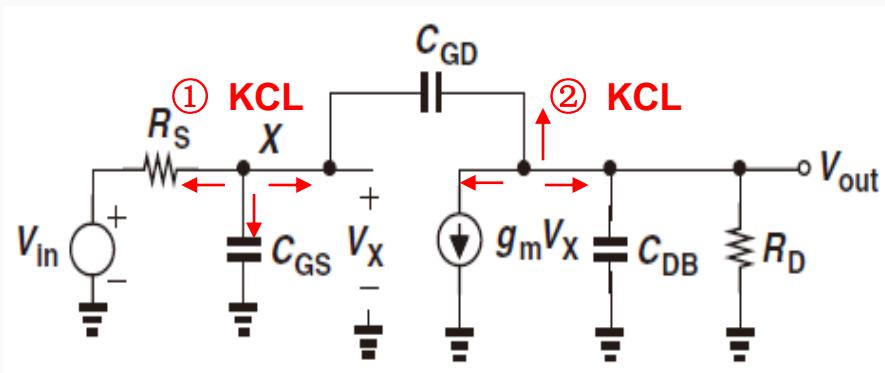
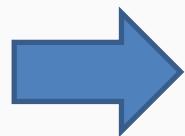
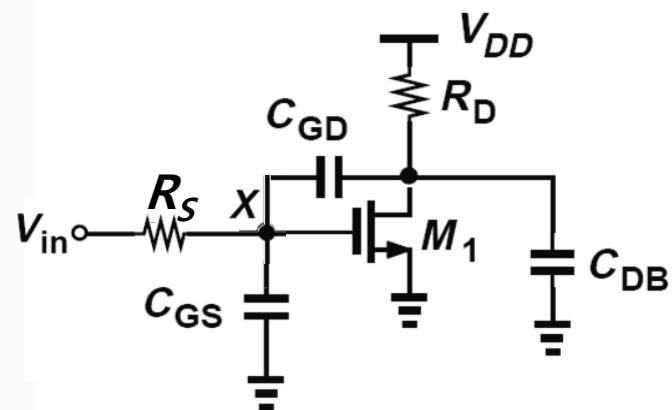
$$\textcircled{2} \quad (V_{out} - V_x) C_{GD}s + g_m V_x + V_{out} \left(\frac{1}{R_D} + C_{DB}s \right) = 0$$

$$\frac{V_{out}}{V_{in}}(s) = \frac{(C_{GD}s - g_m) R_D}{R_s R_D \xi s^2 + [R_s (1 + g_m R_D) C_{GD} + R_s C_{GS} + R_D (C_{GD} + C_{DB})] s + 1}$$

$$(\xi = C_{GS} C_{GD} + C_{GS} C_{DB} + C_{GD} C_{DB})$$



Frequency Response of CS Stage



Use the equivalent circuit

$$\omega_{p1} = \frac{1}{R_s (1 + g_m R_D) C_{GD} + R_s C_{GS} + \boxed{R_D (C_{GD} + C_{DB})}}$$



$$\omega_{p1} = \frac{1}{R_s [C_{GS} + (1 + g_m R_D) C_{GD}] s}$$

$$\omega_{p2} = \frac{R_s (1 + g_m R_D) C_{GD} + R_s C_{GS} + R_D (C_{GD} + C_{DB})}{R_s R_D (C_{GS} C_{GD} + C_{GS} C_{DB} + C_{GD} C_{DB})}$$



$$\omega_{p2} = \frac{1}{R_D \left[C_{DB} + \left(1 + \frac{1}{g_m R_D} \right) C_{GD} \right] s}$$

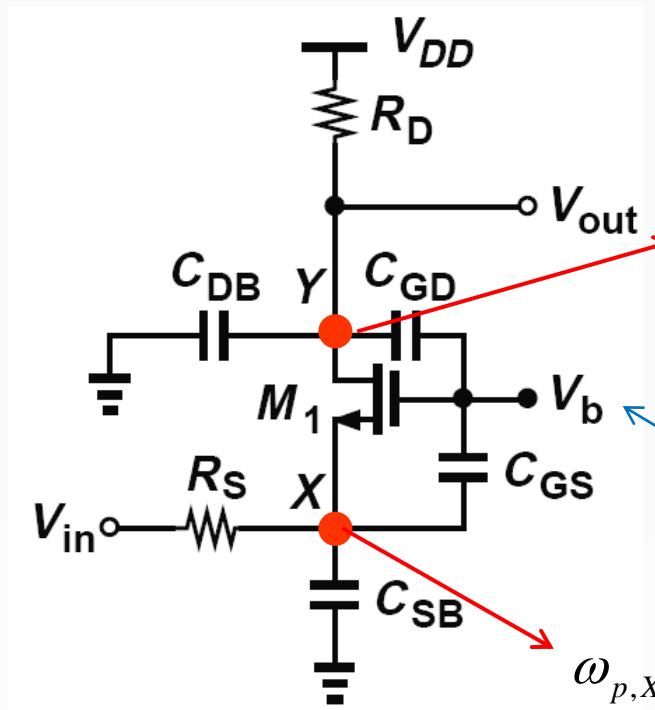
$$\text{If, } C_{GS} \gg \frac{(1 + g_m R_D) C_{GD} + R_D (C_{GD} + C_{DB})}{R_s}$$

$$= \frac{1}{R_D (C_{GD} + C_{DB})}$$



Frequency Response of CG Stage

$$r_o = \infty$$



$$\omega_{p,Y} = \frac{1}{R_D(C_{GD} + C_{DB})}$$

ac ground

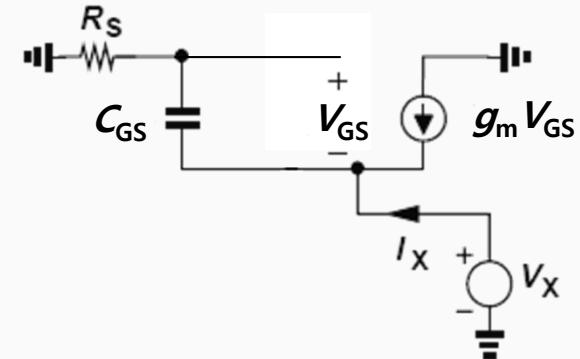
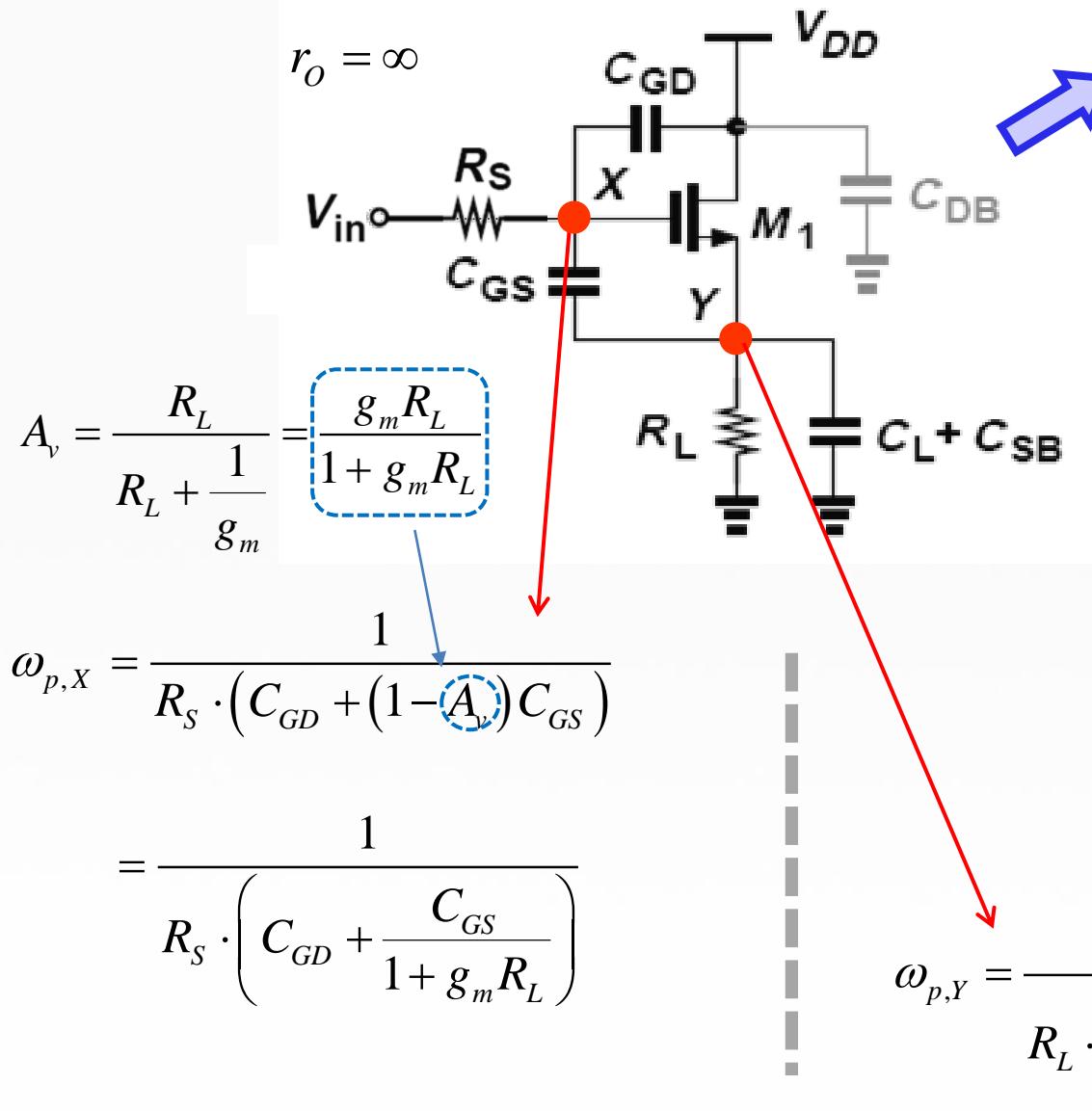
$$\omega_{p,X} = \frac{1}{\left(R_S \parallel \frac{1}{g_m} \right) (C_{GS} + C_{SB})}$$



High speed(No miller effect)



Frequency Response of SF Stage



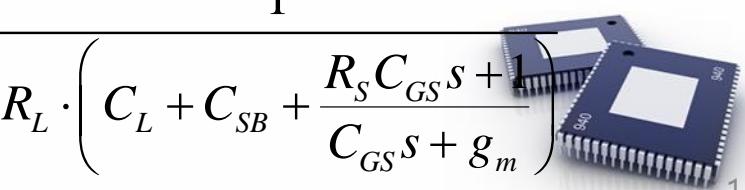
$$(I_X + g_m V_{GS}) \left(\frac{1}{C_{GS}s} \right) = -V_{GS}$$

$$V_{GS} = -I_X \frac{1}{C_{GS}s + g_m}$$

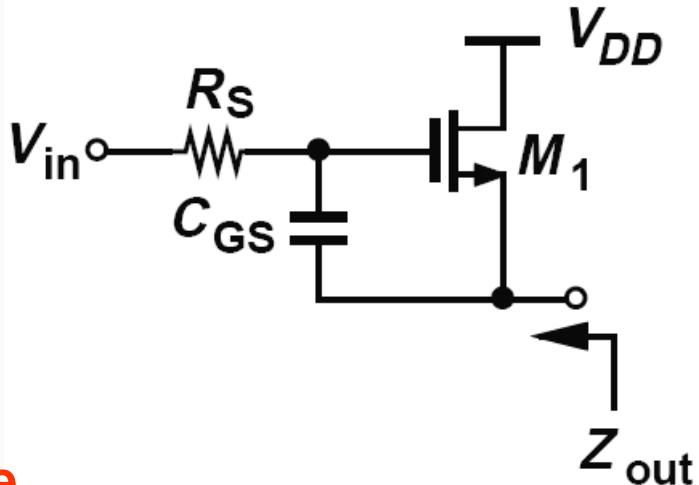
Using KVL

$$(I_X + g_m V_{GS}) R_s - V_{GS} = V_X$$

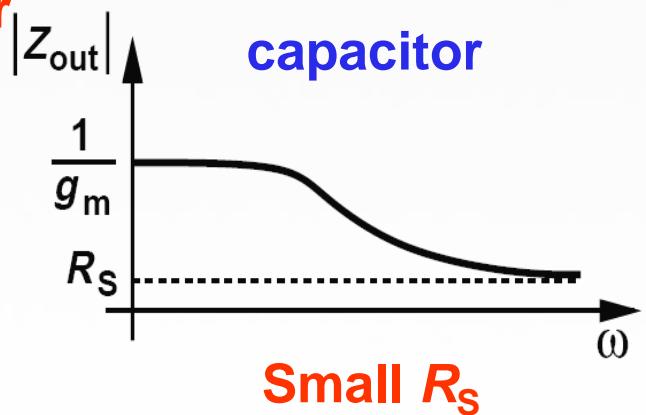
$$\therefore Z_{out} = \frac{V_X}{I_X} = \frac{R_s C_{GS} s + 1}{C_{GS}s + g_m}$$



Output Impedance of Source Follower

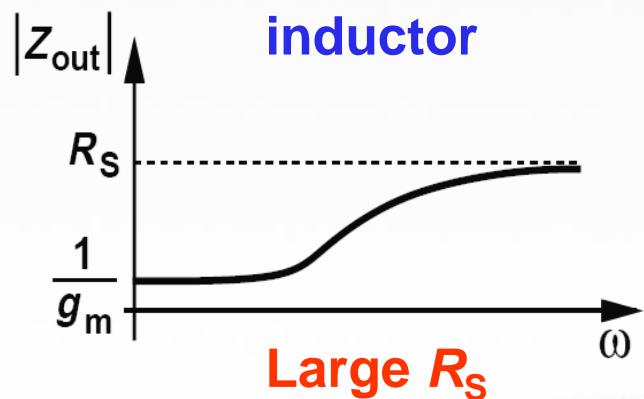


Source
Follower

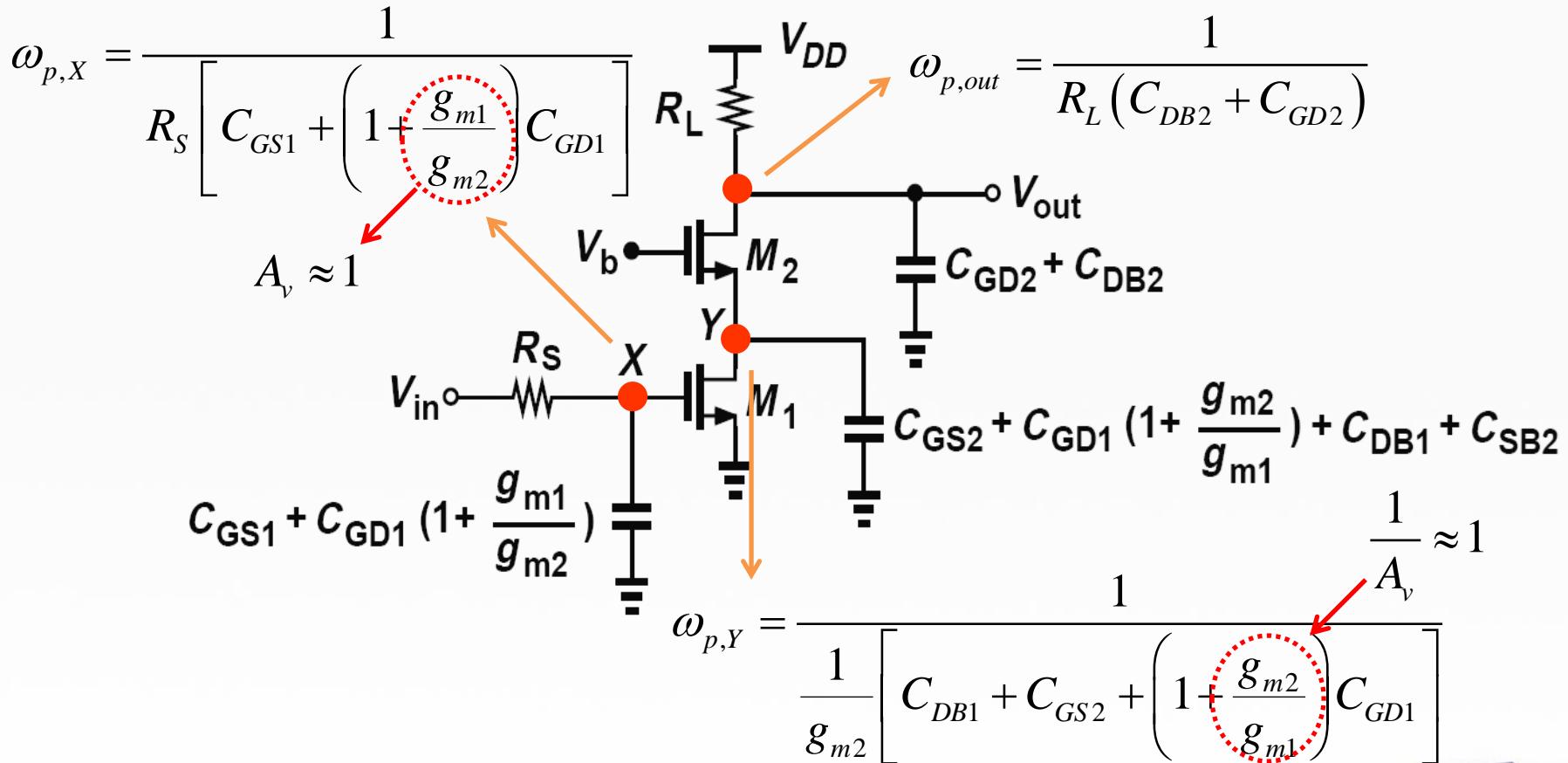


$$Z_{out} = \frac{V_X}{I_X} = \frac{R_S C_{GS} s + 1}{C_{GS} s + g_m}$$

$\left. \begin{array}{l} s = 0 \Rightarrow Z_{out} = \frac{1}{g_m} \\ s = \infty \Rightarrow Z_{out} = R_s \end{array} \right\}$



Poles of MOS Cascode

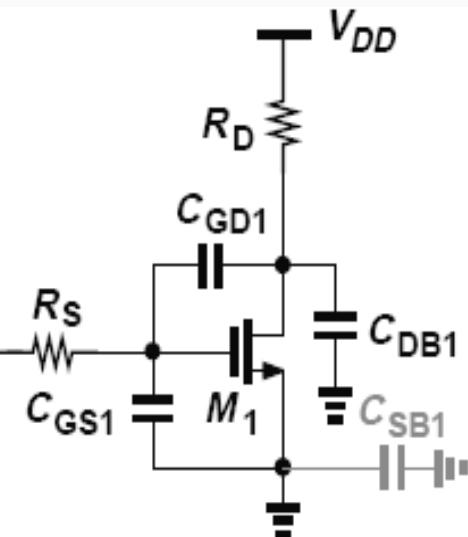
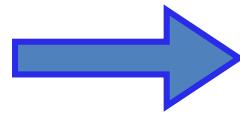
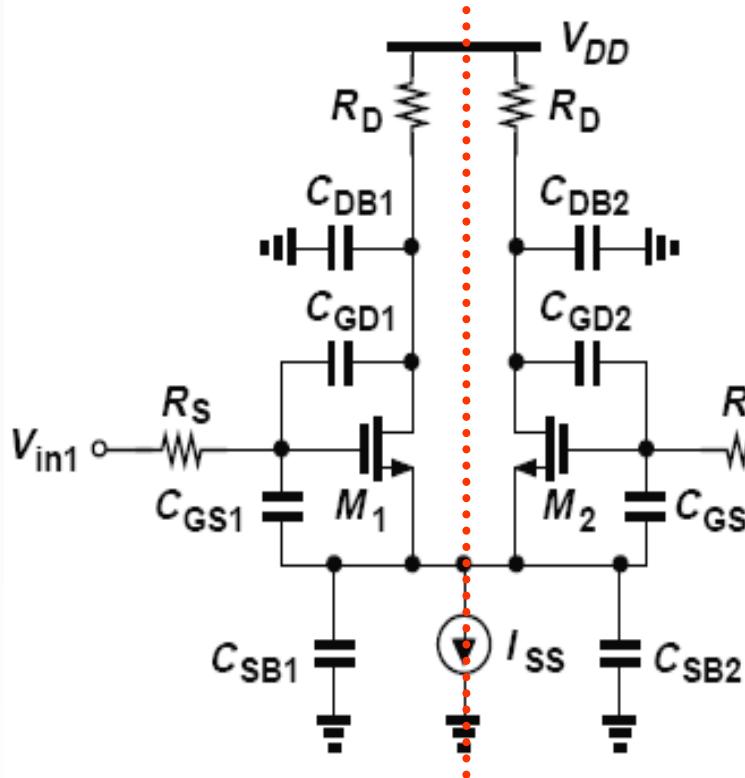


😊 High speed(reject the miller effect)



Differential Pair Frequency Response

Symmetric



Half Circuit

its transfer function, I/O impedances, locations of poles/zeros are the same as that of the half circuit's.

